# Education

Monae Edmead

340-344-0110 *|* [monaeedmead@gmail.com](mailto:monaeedmead@gmail.com%20)

**Kennesaw State University** Kennesaw, GA

*Bachelor of Science in Computer Engineering, Minor in Cybersecurity Aug. 2019 – May 2023*

# Experience

**IT Intern** June 2021 – Sept. 2021

*Office of the Lieutenant Governor St. Thomas, VI*

* Helped workers in the office troubleshoot problems they had with their computers
* Swapped out the hard drives in the office for better performance.
* Trained another intern.
* Completed other tasks when given.

**Student Assistant** Sep. 2020 – Aug. 2022

*Kennesaw State University Marietta, GA*

* Helped students find textbooks in the store.
* Answered the phone when needed.
* Answered customers’ questions.
* Organize inventory.

**Student Assistant** Aug. 2022 –Present

*Kennesaw State University Marietta, GA*

* Direct traffic in the office.
* Answer the phone when needed.
* Help with events.

# Projects

**Detection Device** *| Ultrasonic sensors, Arduino, Joystick, Passive Buzzer* March 2022 – May 2022

* Used an ultrasonic sensor to detect the distance of an object.
* Connect the joystick to an Arduino to move the device using a servo motor.
* The passive buzzer would be activated if the object was within 10 cm of the device.

**Handwriting Recognition System** *| Raspberry Pi, Python, Camera, Sense Hat* October 2022 – December 2022

* Using a camera connected to the Raspberry Pi, a Convolutional Neural Network determined the number on a piece of paper.
* Once the number was detected the value was displayed on the Raspberry Pi Sense hat.

# Skills

**Languages**: Java, Python, C/C++, HTML, JavaScript

**Hardware**: Arduino, Raspberry Pi

# Classes

* Digital Logic Design
* Circuits Analysis I
* Engineering Electronics
* Data Structures
* Client Systems Security
* Computer Organization and Interfacing
* Neural Networks Machine Learning
* Advanced Embedded Systems
* Data Collection and Analysis
* C++ Programming for Engineers
* Control Systems
* Network Security
* VHDL Design with FPGAs